



MACHINE LEARNING FOR ELECTRONICS ENGINEERING

Does applied research sound interesting to you?

Join the KIRESys project to apply machine learning-based document mining and natural language processing (NLP) for building a knowledge base for next generation electronics engineering tools.

We are looking for motivated (master) students who are willing to assist in project work as SHK/WHK (10h/week), and who may consider writing their thesis in a project-related topic.

You should have some programming experience (ideally Python) and know how to use GitHub/GitLab & Docker. Interest in data science, information extraction, or electronics engineering is a plus.

Contact: Frank Binder, binder@infai.org
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Web: infai.org/kiresys/



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We offer an SHK/WHK position (10h/week) and potential master's thesis topics

Interested in latest ML approaches to document mining and knowledge base construction?

Eager to contribute to next generation CAD tools for electronics engineering?

Familiar with Python, Git & Docker - and OK with working mostly remotely?

Laptop provided if necessary



PLEASE CONTACT:

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